

2018 Creative STORAGESM CONFERENCE

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AN ENTERTAINMENT STORAGE ALLIANCE™ EVENT



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TITLE: The Bus Matters: Why PCIe® technology should be your storage interface of choice for digital content storage and delivery.

ABSTRACT

As content creation and consumption continues to grow, how digital media is written and read to Nonvolatile Media becomes even more critical. NVMe® over PCI Express® (PCIe®) 3.0 architecture has enabled Flash Storage to transition to high speed, low latency power efficient performance over the past few years. PCIe 3.0 technology enables the best performance from today's NAND drives as well as up-and-coming Storage-Class-Memory (SCM) drives. And as the hunger for additional performance in power constrained environments continues, PCI-SIG® continues its history of doubling performance while delivering additional features with the development of the PCIe 4.0 and PCIe 5.0 specifications.

This presentation will review the major benefits and features of NVMe and PCIe 3.0 architecture for today's storage solutions and then preview PCIe 4.0 and PCIe 5.0 technology, which will continue to enable power efficient performance required as NAND capacities scale and faster SCM (Storage Class Memories) become mainstream. Session attendees will gain insight into the current status of the PCIe 4.0 technology rollout and testing and will learn about the PCIe 5.0 specification development and timeline for completion in 2019.

BIOGRAPHY

Justin Wenck, Ph.D. is a Senior Technical Marketing Engineer in the Datacenter SSD Technical Marketing Group at Intel Corporation responsible for PCIe NVMe SSDs. He has also worked as a validation lead ensuring SSDs were PCI-SIG compliant, and a circuit designer for memory interfaces. Dr. Wenck holds Ph.D. and M.S. degrees in electrical and computer engineering from the University of California-Davis and a B.S. in electrical engineering from California Polytechnic State University, San Luis Obispo.